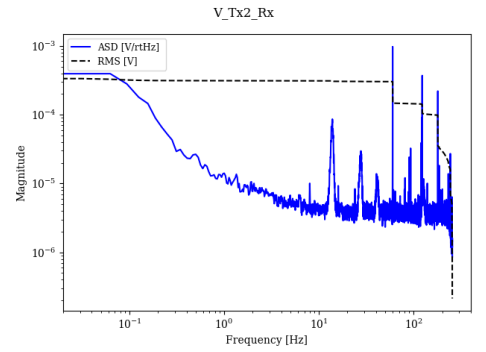
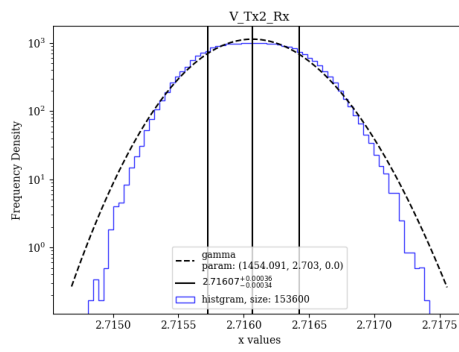
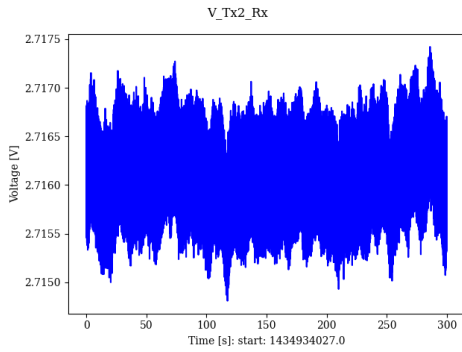
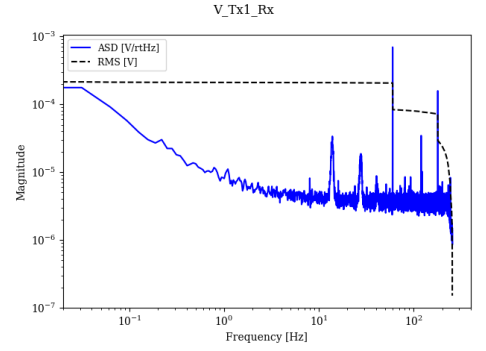
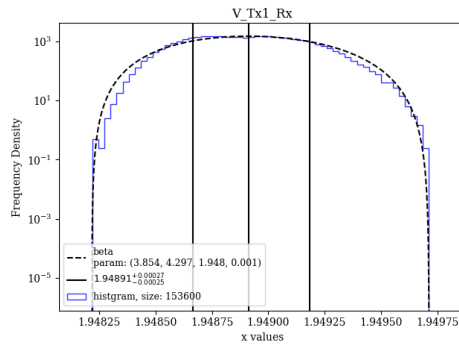
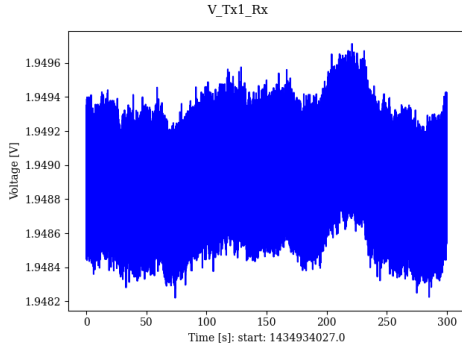
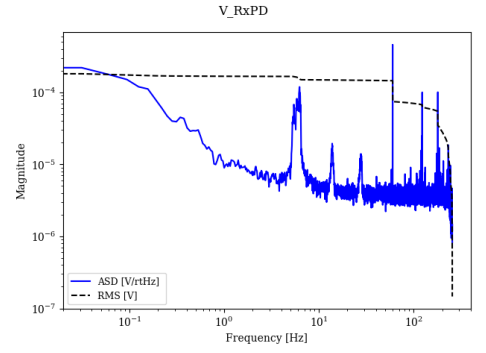
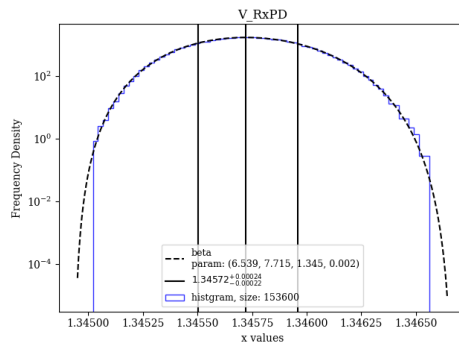
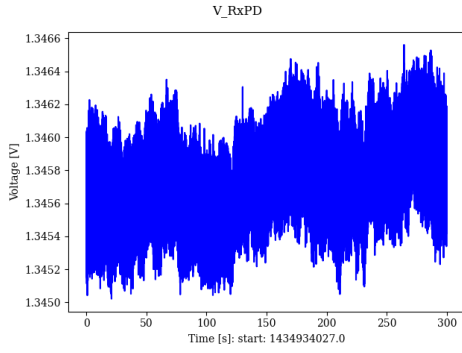


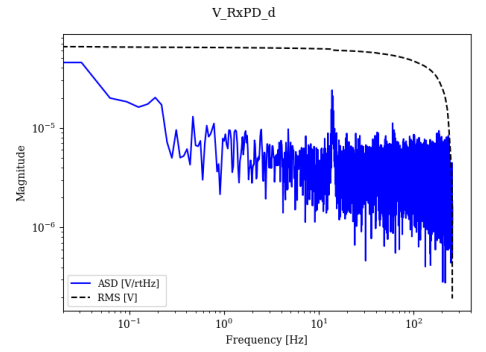
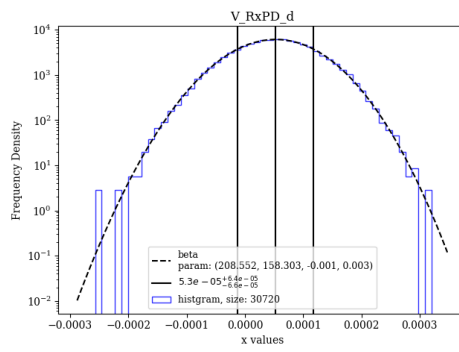
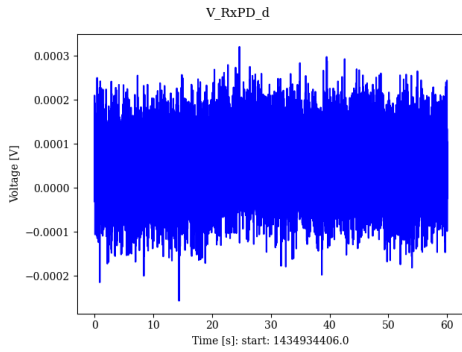
## Xpcal Calibration Parameters (2025-06-26)

|                   |                                |
|-------------------|--------------------------------|
| V_Tx1_bg:         | -0.001068 ± 0.000602 (-56.36%) |
| V_Tx2_bg:         | -0.001094 ± 0.000601 (-54.95%) |
| V_Rx_bg:          | 0.000052 ± 0.000065 (125.74%)  |
| V_Tx1_bg_true:    | 0.006231 ± 0.000602 (9.66%)    |
| V_Tx2_bg_true:    | 0.005251 ± 0.000601 (11.45%)   |
| V_Rx_bg_true:     | -0.003732 ± 0.000065 (-1.75%)  |
| alpha_RxPDpWSK_1: | 0.157680 ± 0.000041 (0.03%)    |
| alpha_RxPDpWSK_2: | 0.157576 ± 0.000044 (0.03%)    |
| alpha_TxPD1pWSK:  | 0.443332 ± 0.000020 (0.00%)    |
| alpha_TxPD2pWSK:  | 0.603488 ± 0.000023 (0.00%)    |
| e_1:              | 0.964835 ± 0.000157 (0.02%)    |
| e_2:              | 0.952553 ± 0.000232 (0.02%)    |
| SP_1:             |                                |
| cal_SP_P_norm:    | 0.501601 ± 0.000146 (0.03%)    |
| cal_SP_V_norm:    | 0.501722 ± 0.000152 (0.03%)    |
| SP_RxPD_norm_dan: | 0.501800 ± 0.004356 (0.87%)    |
| SP_2:             |                                |
| cal_SP_P_norm:    | 0.498399 ± 0.000179 (0.04%)    |
| cal_SP_V_norm:    | 0.498278 ± 0.000146 (0.03%)    |
| SP_RxPD_norm_dan: | 0.498200 ± 0.004356 (0.87%)    |
| G2:               | 0.952181 ± 0.016591 (1.74%)    |

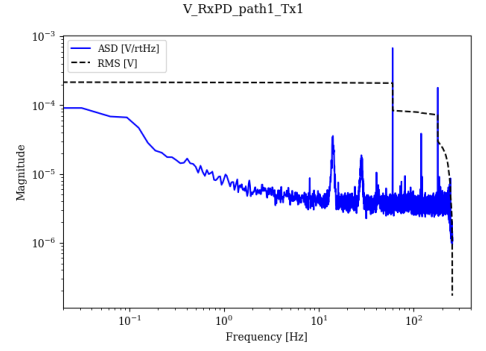
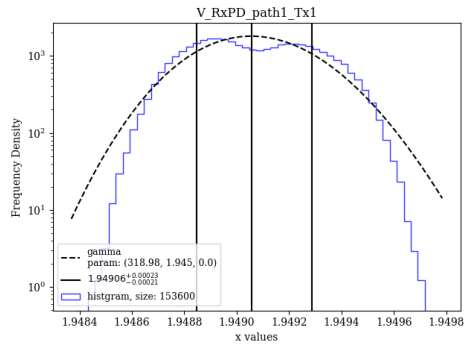
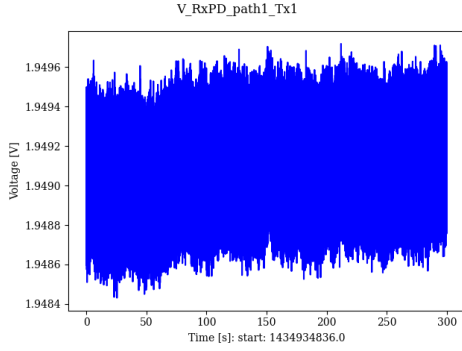
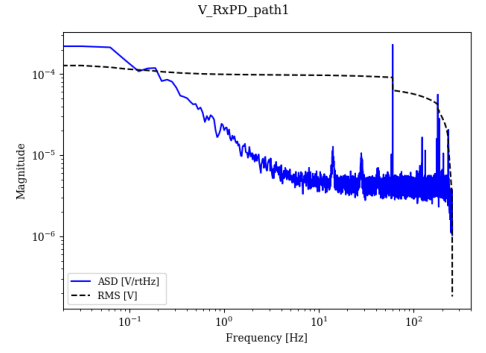
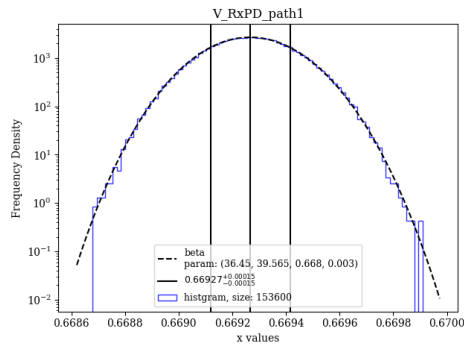
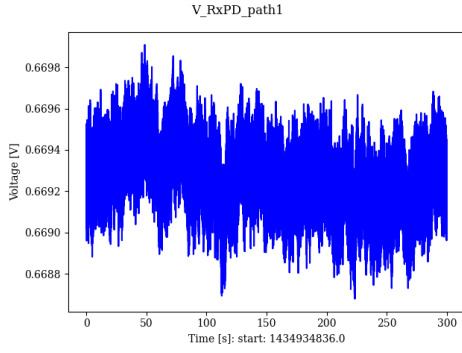
## PXPD\_WITH\_2\_BEAMS



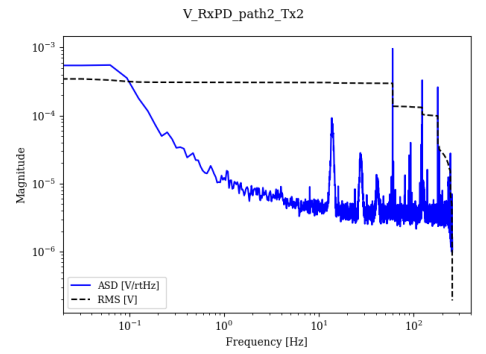
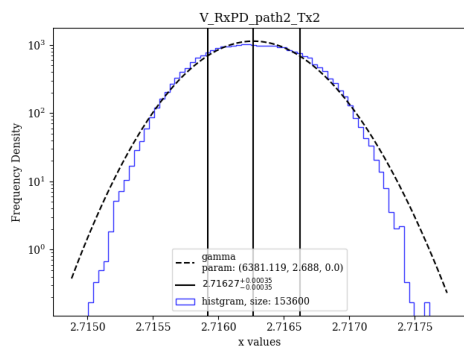
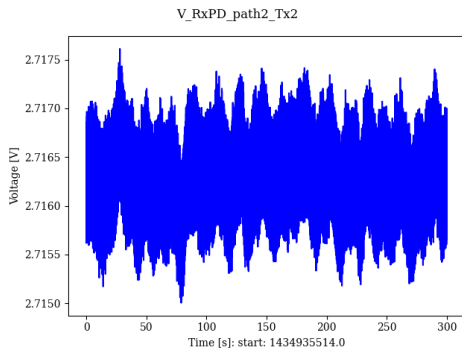
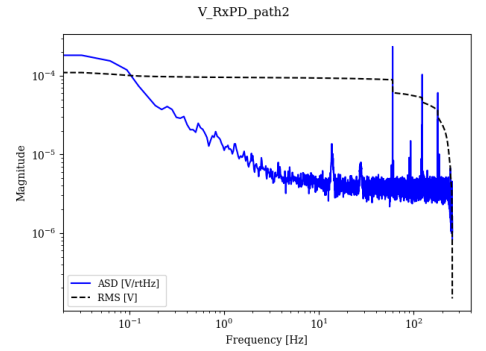
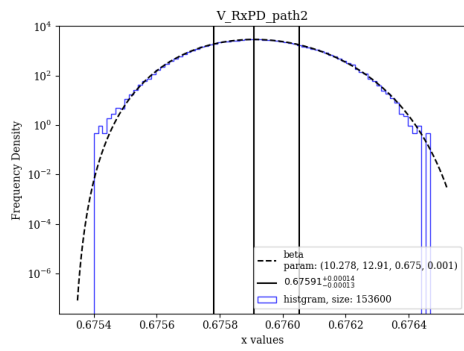
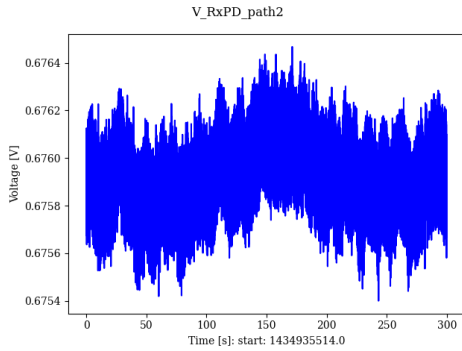
## PXPD\_WITH\_NO\_BEAM



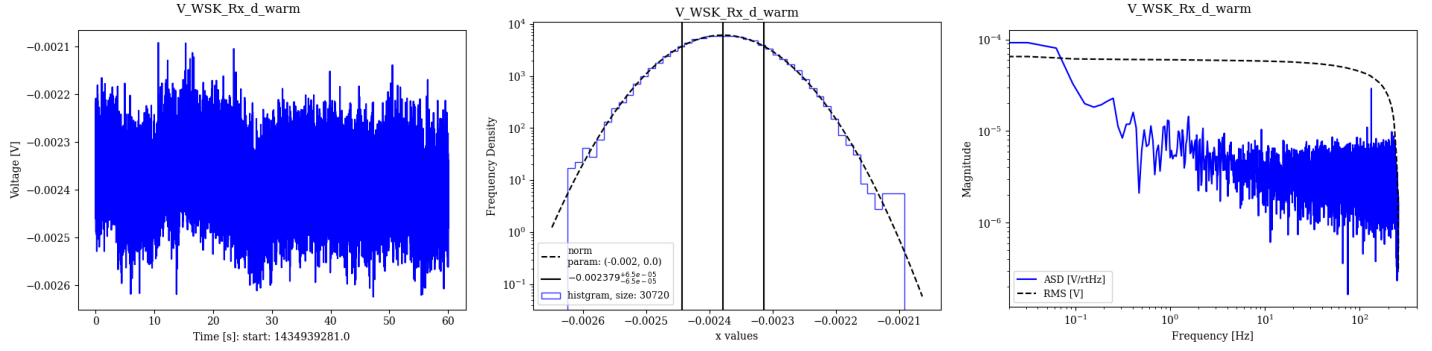
## PXPD\_WITH\_PATH1



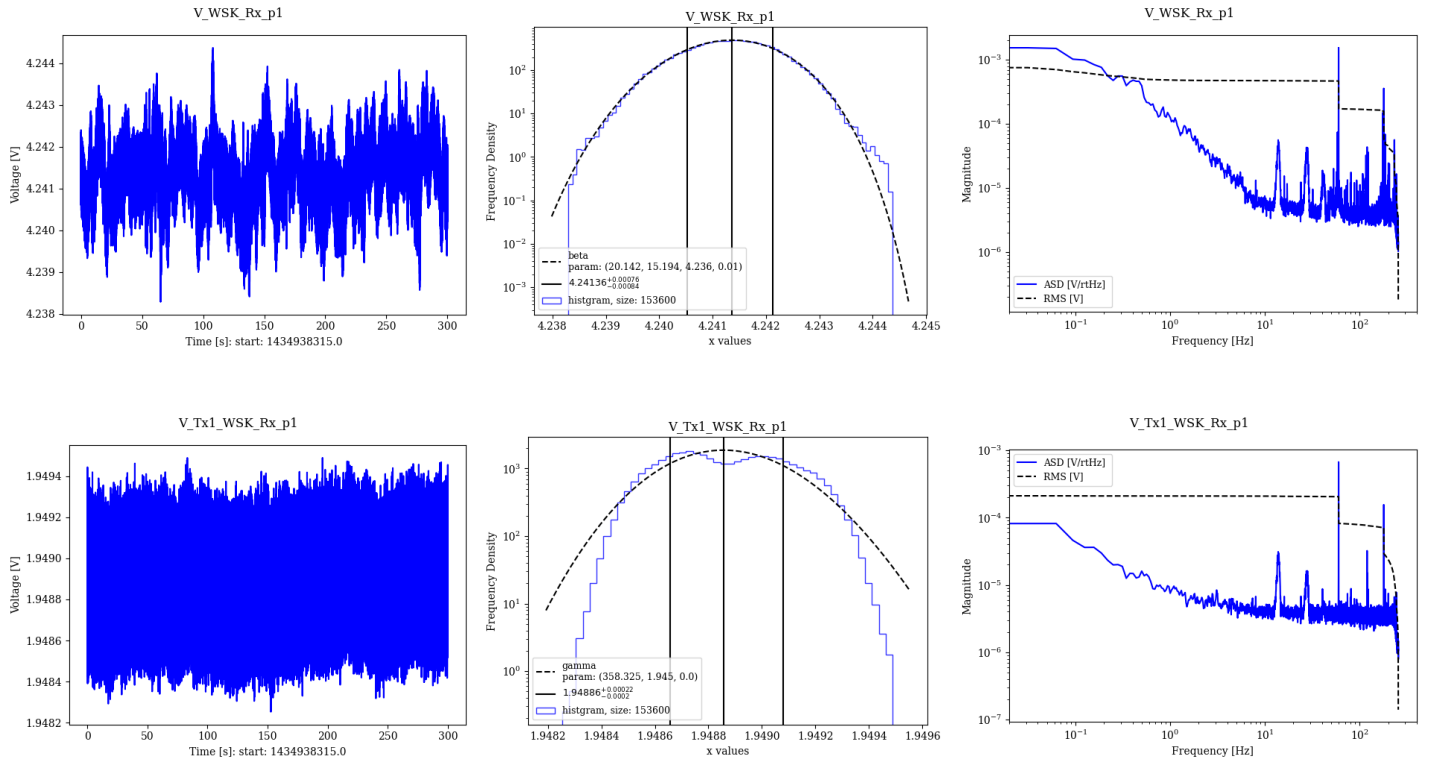
## PXPD\_WITH\_PATH2



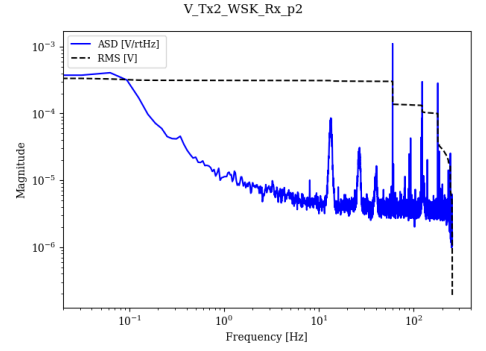
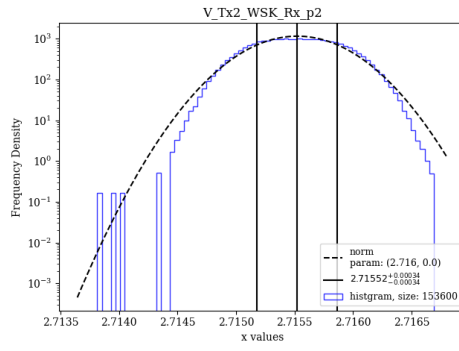
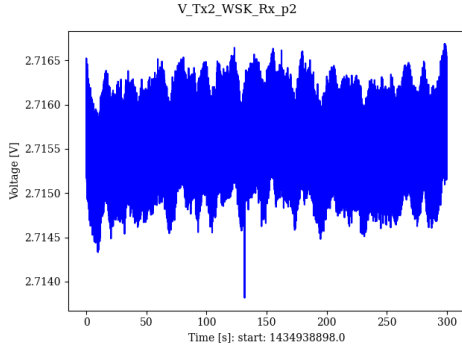
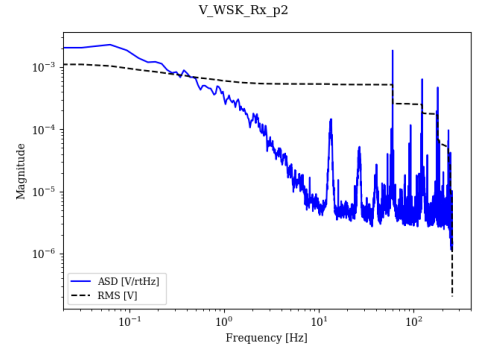
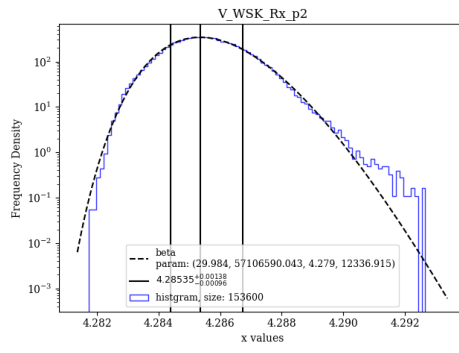
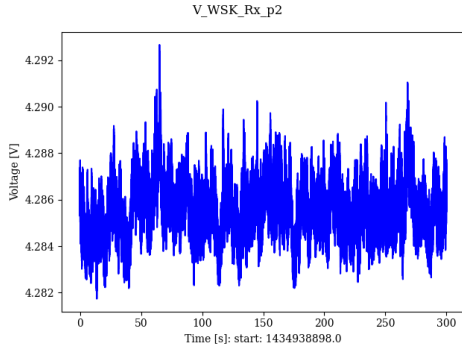
## WSK\_WITH\_NO\_BEAM\_AT\_RX\_WARM



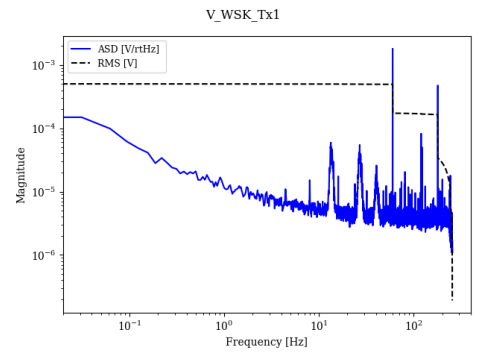
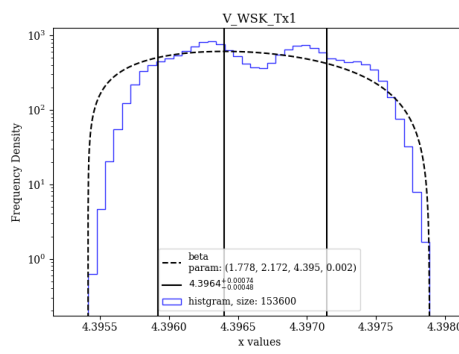
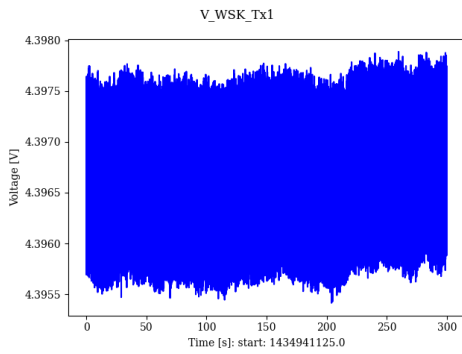
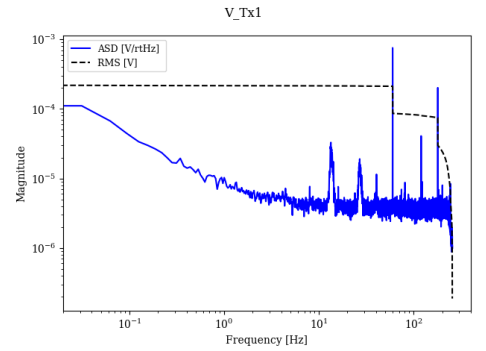
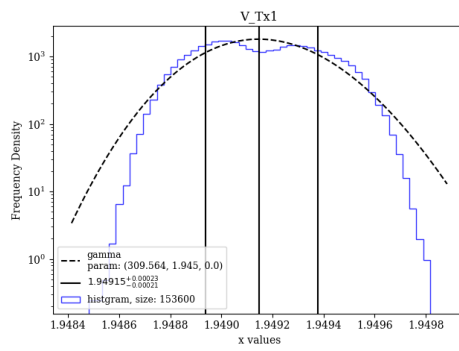
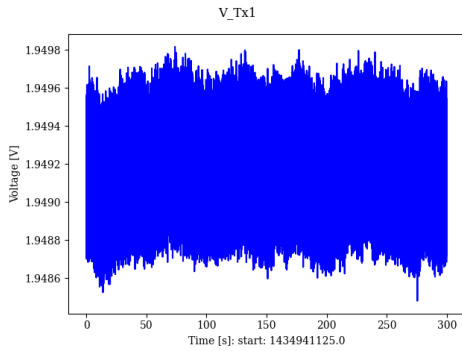
## WSK\_WITH\_PATH\_1\_AT\_RX



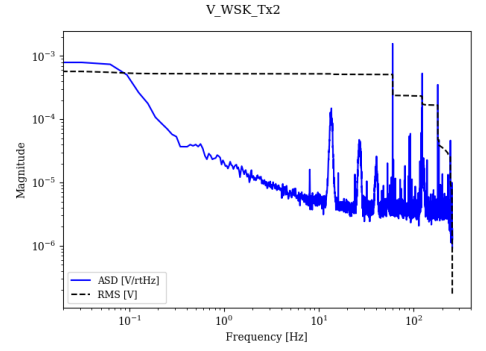
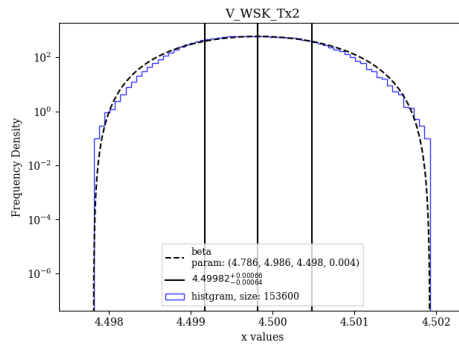
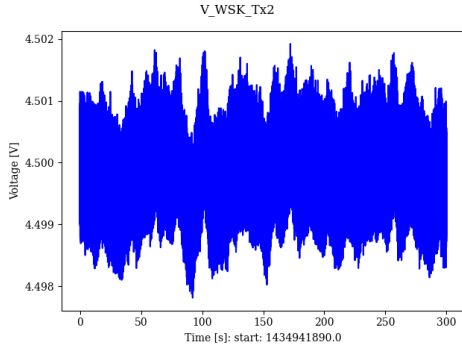
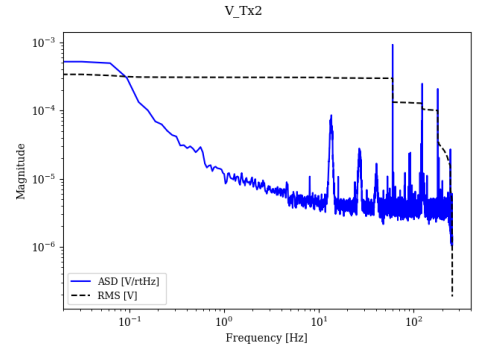
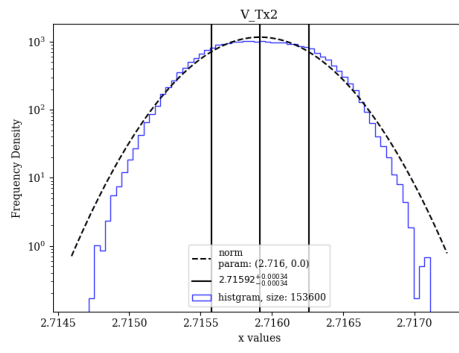
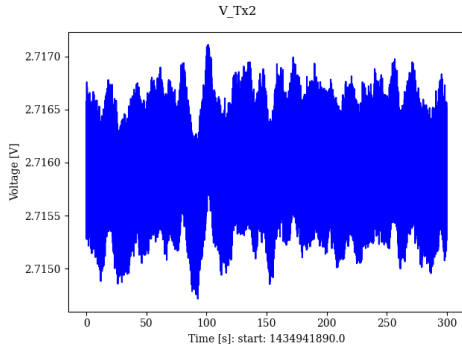
## WSK\_WITH\_PATH\_2\_AT\_RX



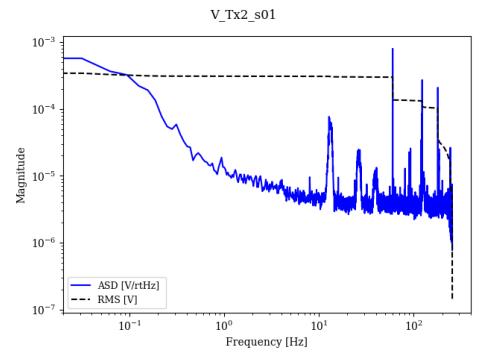
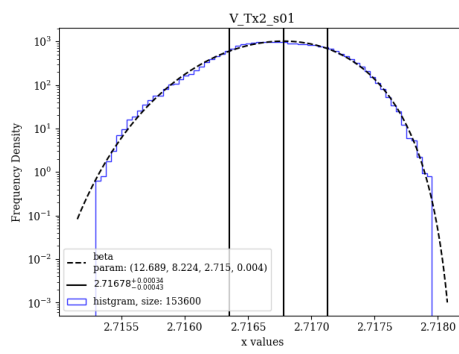
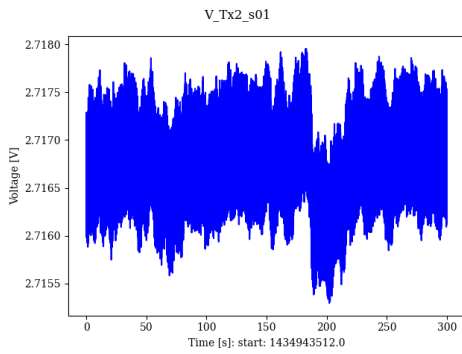
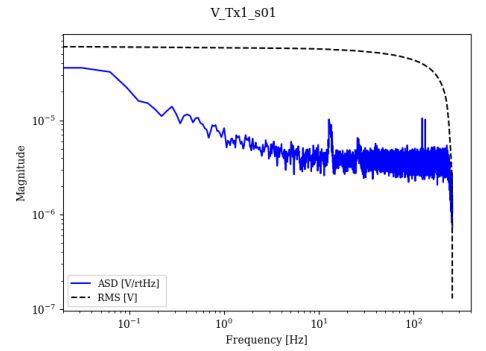
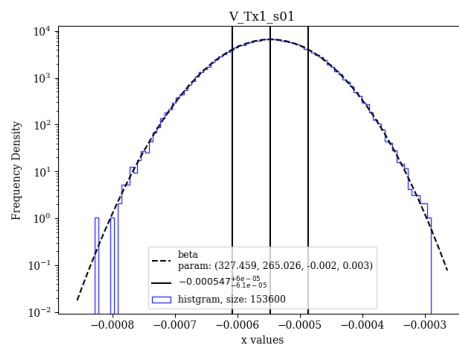
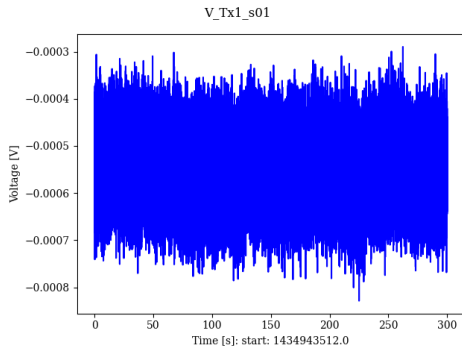
## TXPD1\_AND\_WSK



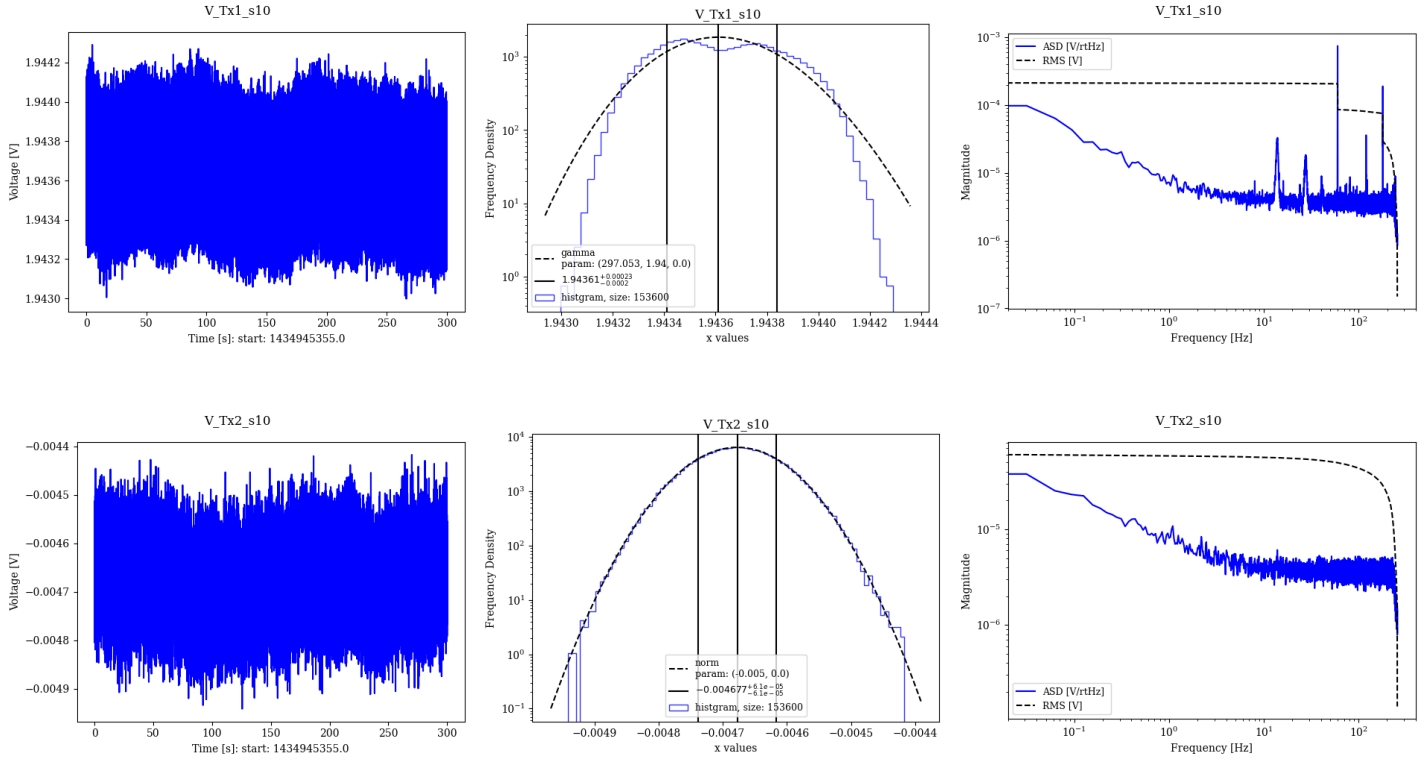
## TXPD2\_AND\_WSK



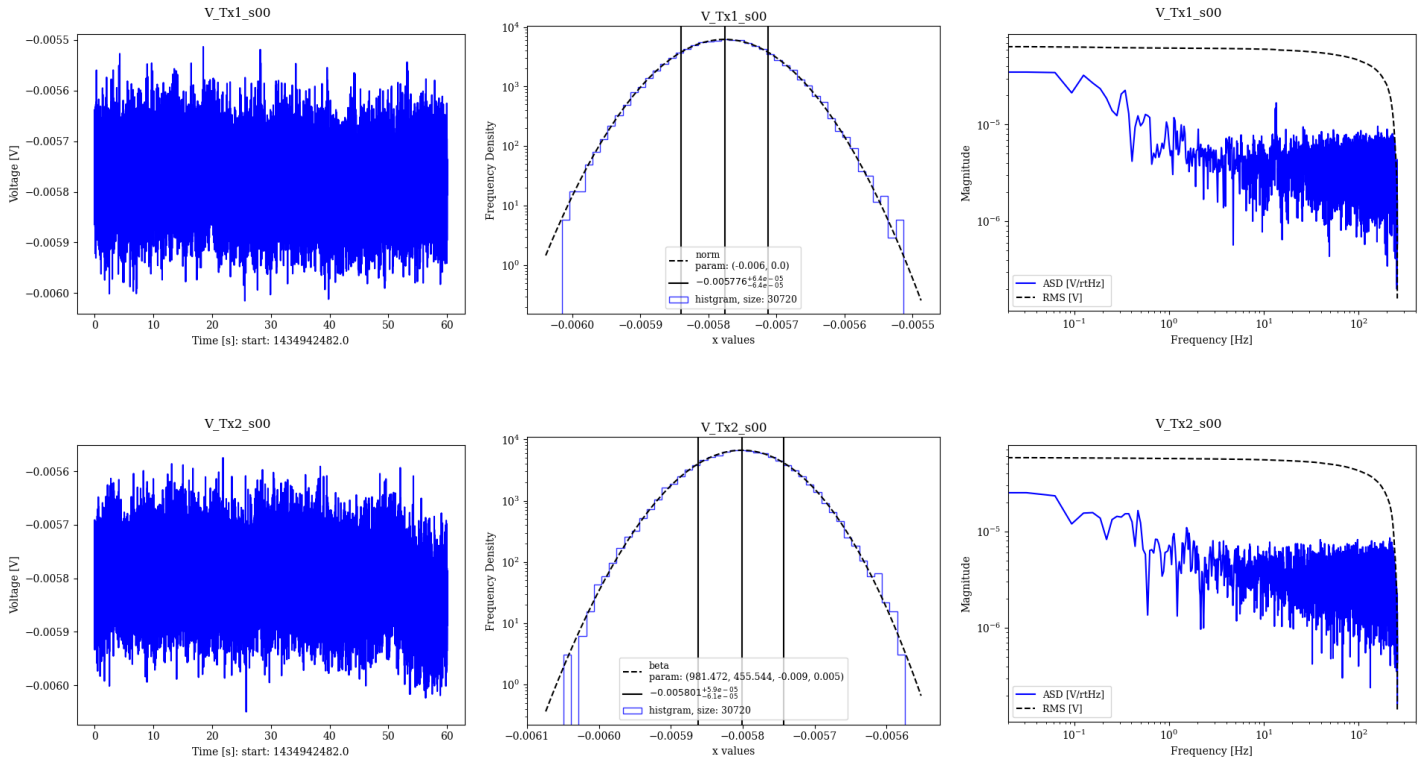
## TXPD1\_AND\_TXPD2\_S01



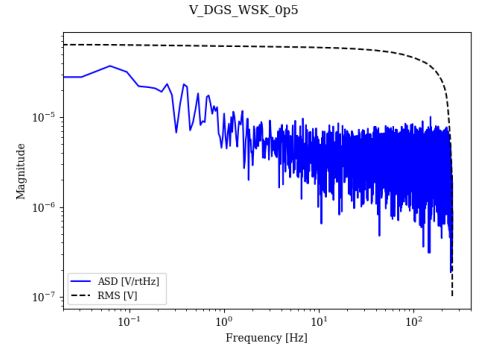
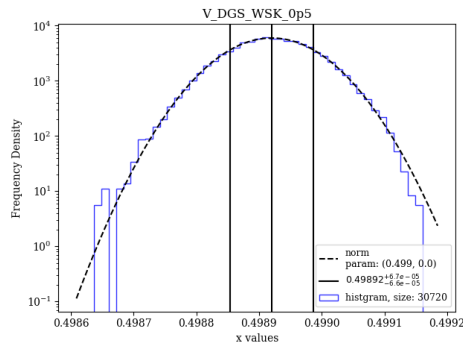
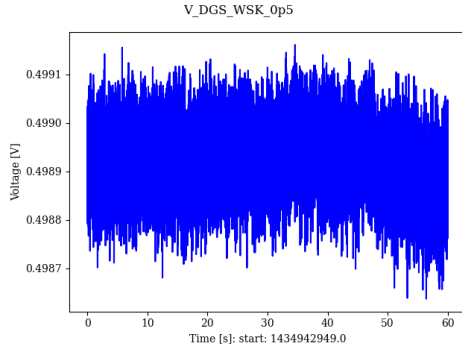
## TXPD1\_AND\_TXPD2\_S10



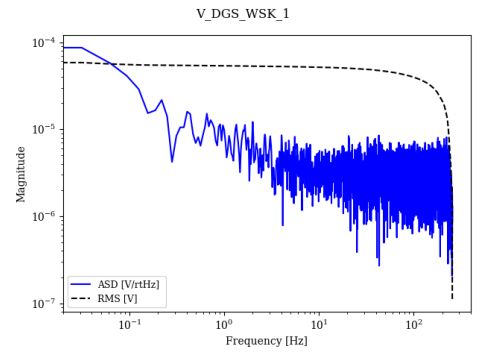
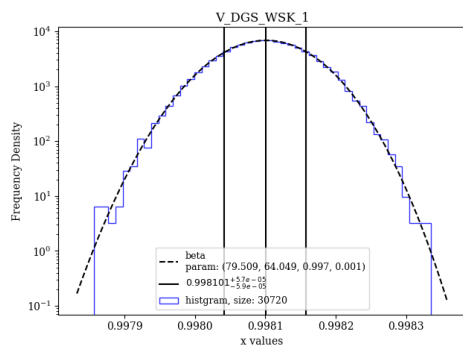
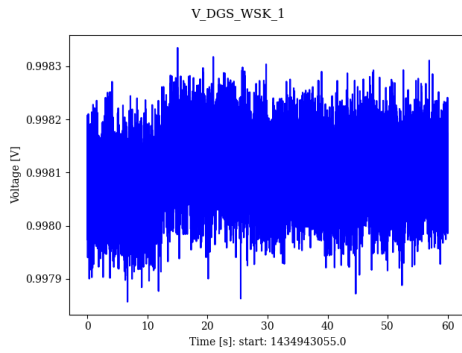
## TXPD1\_AND\_TXPD2\_FOR\_TS60



## WSK\_PORT\_0P5



## WSK\_PORT\_1



## WSK\_PORT\_5

