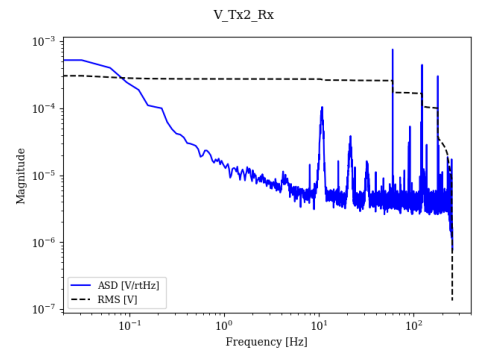
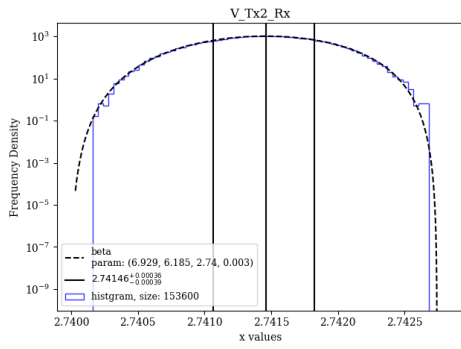
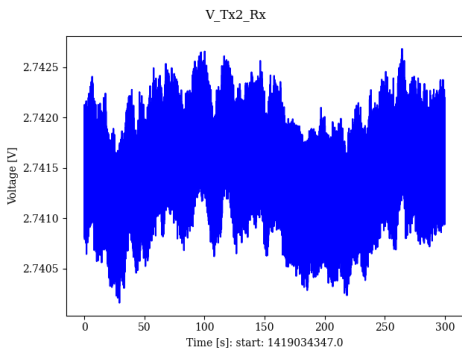
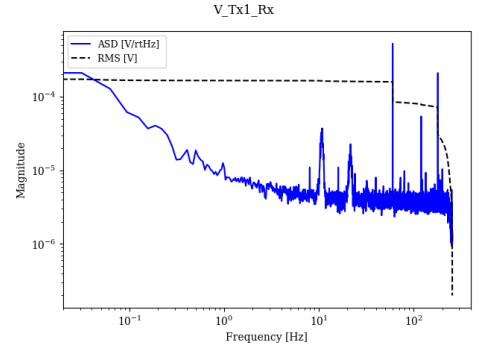
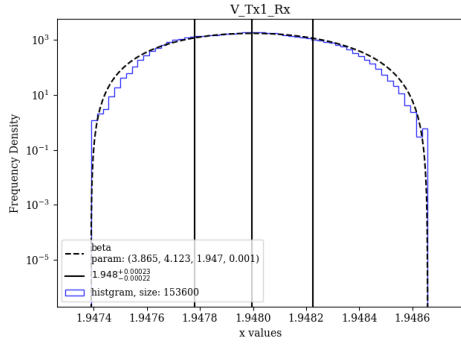
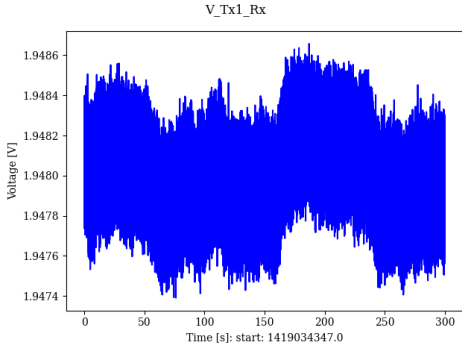
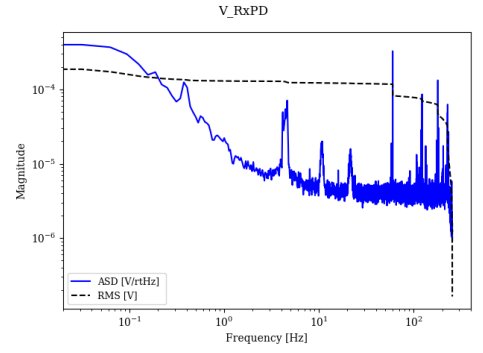
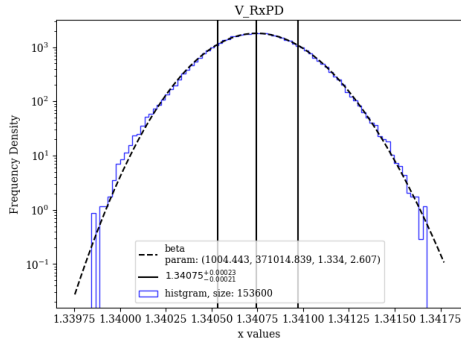
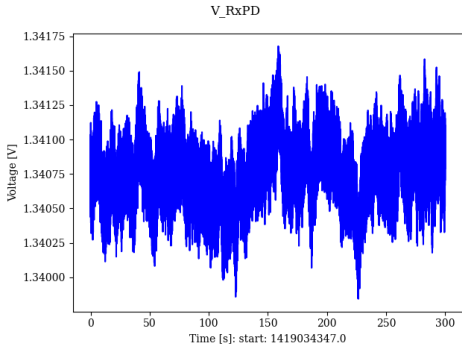


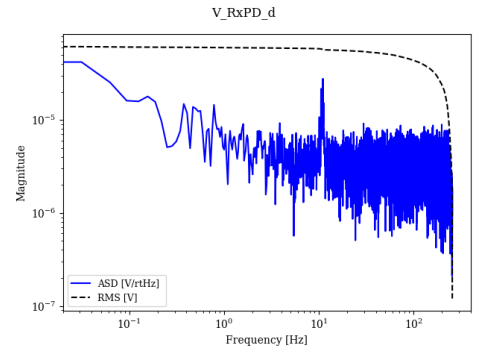
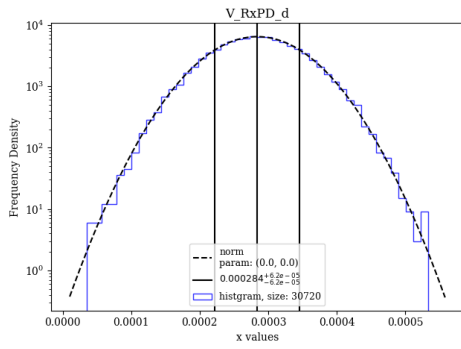
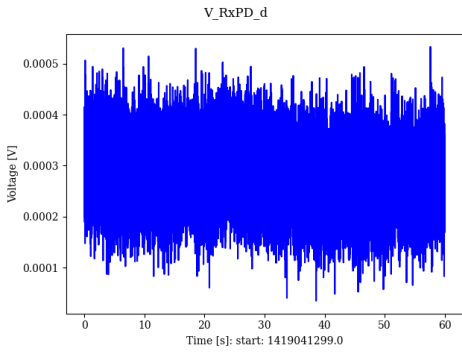
## Xpcal Calibration Parameters (2024-12-24)

V_Tx1_bg:	-0.002849 ± 0.000549 (-19.29%)
V_Tx2_bg:	0.003904 ± 0.000551 (14.10%)
alpha_RxPDpWSK_1:	0.156746 ± 0.000067 (0.04%)
alpha_RxPDpWSK_2:	0.155860 ± 0.000053 (0.03%)
alpha_TxPD1pWSK:	0.443911 ± 0.000019 (0.00%)
alpha_TxPD2pWSK:	0.603179 ± 0.000025 (0.00%)
e_1:	0.964822 ± 0.000297 (0.03%)
e_2:	0.951083 ± 0.000172 (0.02%)
SP_1:	
cal_SP_P_norm:	0.501641 ± 0.000186 (0.04%)
cal_SP_V_norm:	0.503360 ± 0.000193 (0.04%)
SP_2:	
cal_SP_P_norm:	0.498359 ± 0.000152 (0.03%)
cal_SP_V_norm:	0.496640 ± 0.000198 (0.04%)

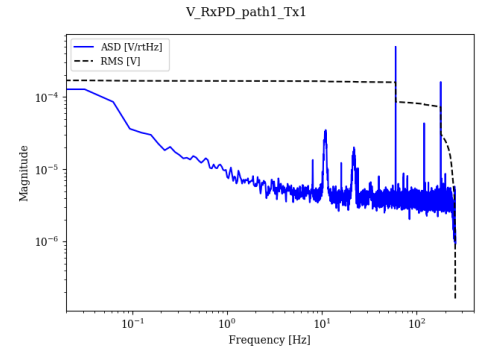
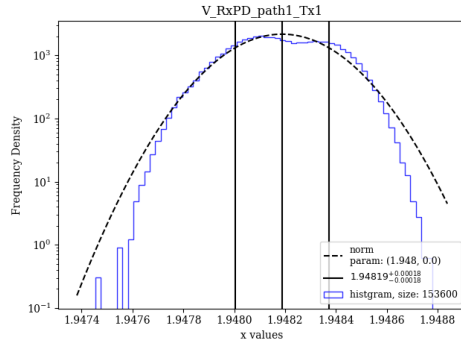
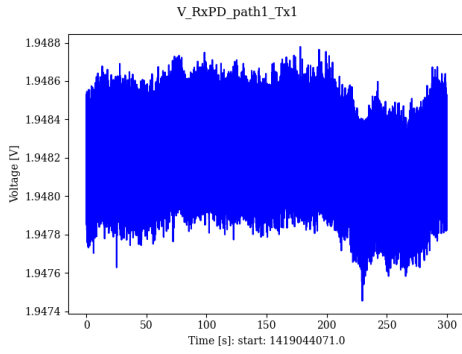
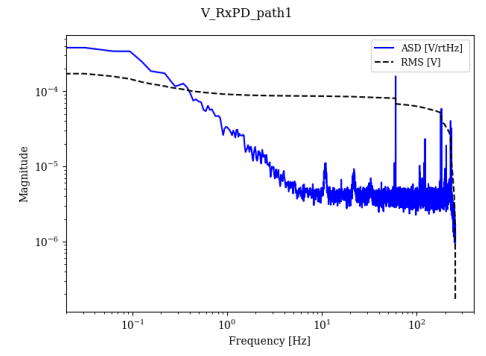
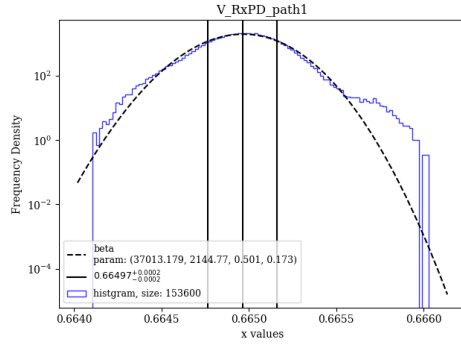
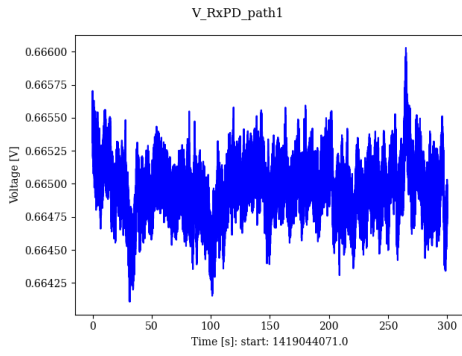
## PXPD\_WITH\_2\_BEAMS



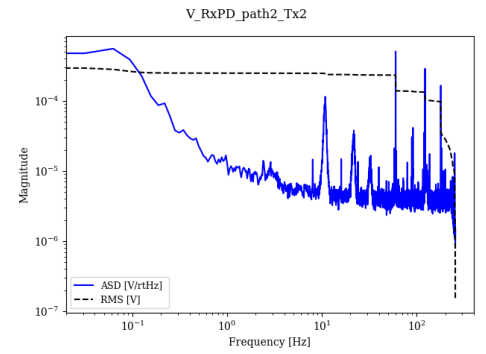
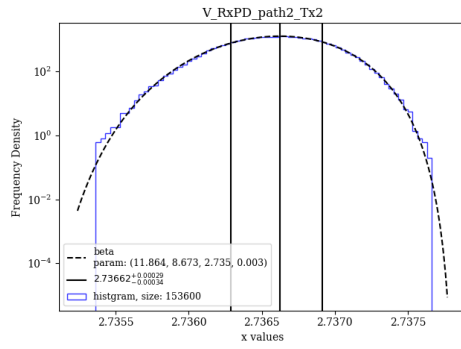
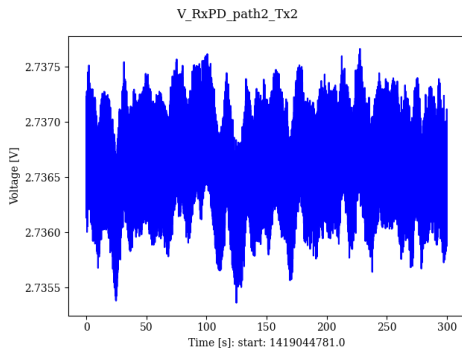
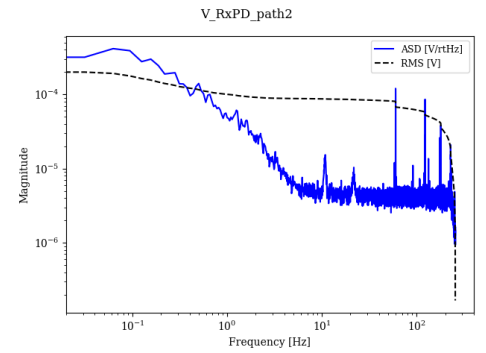
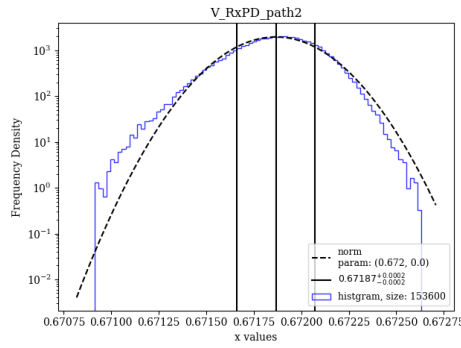
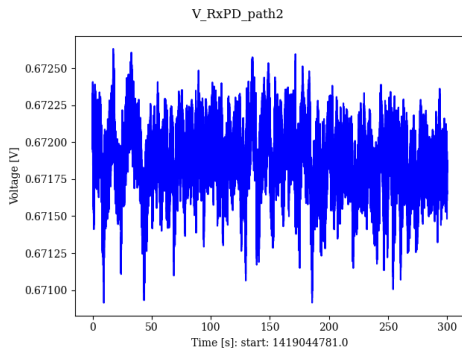
## PXPD\_WITH\_NO\_BEAM



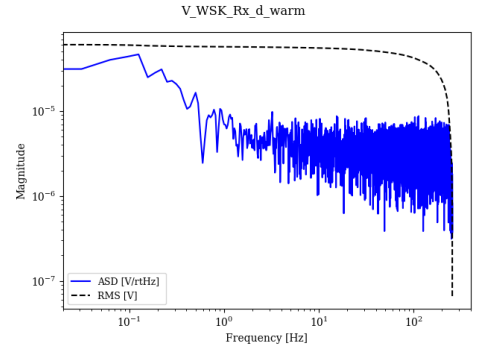
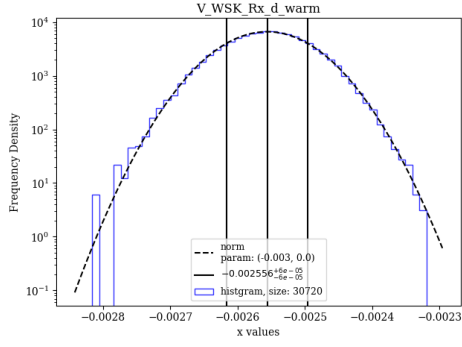
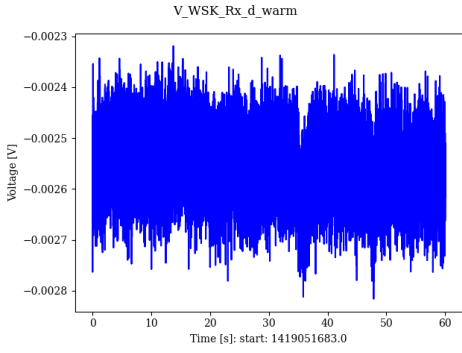
## PXPD\_WITH\_PATH1



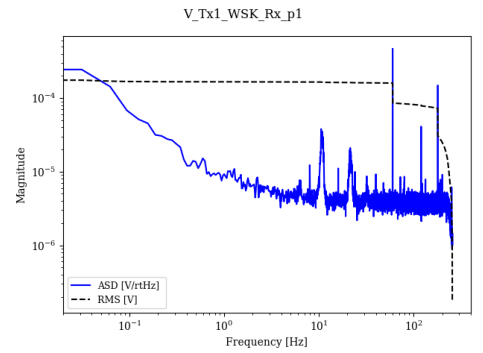
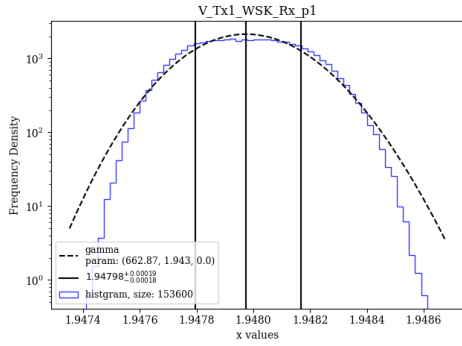
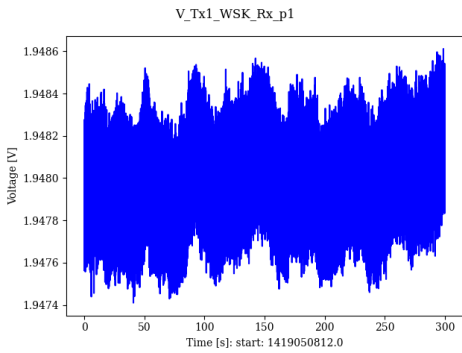
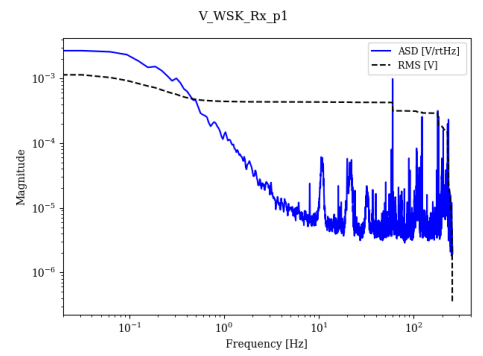
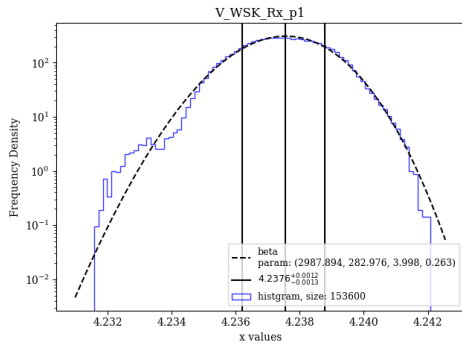
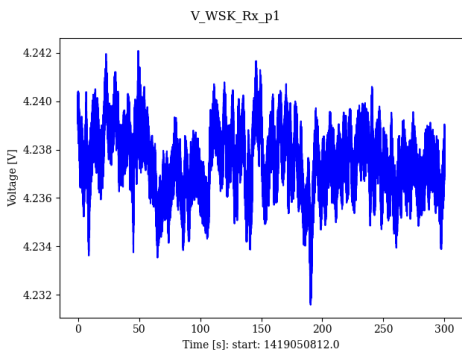
## PXPD\_WITH\_PATH2



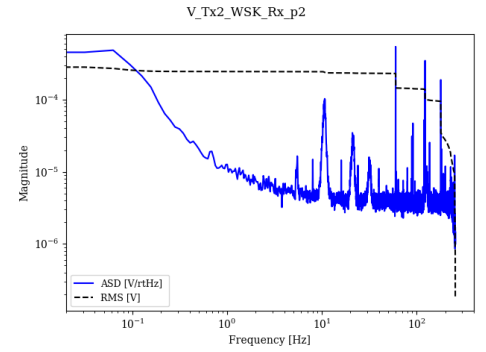
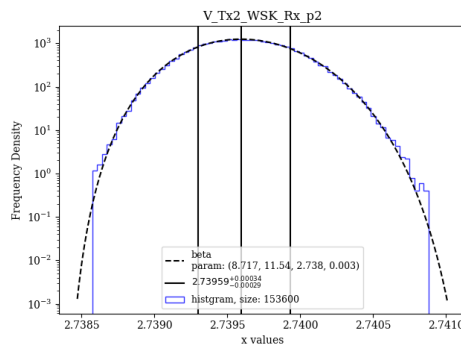
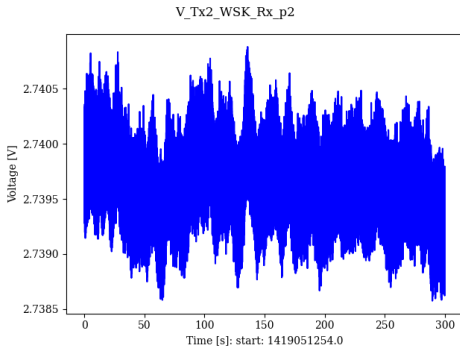
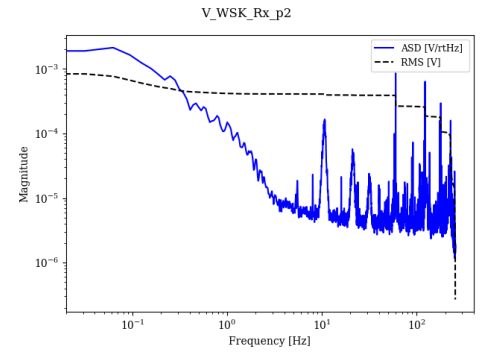
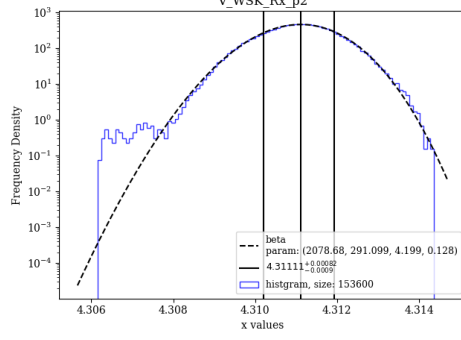
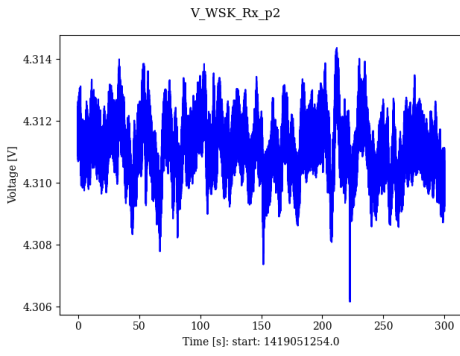
# WSK\_WITH\_NO\_BEAM\_AT\_RX\_WARM



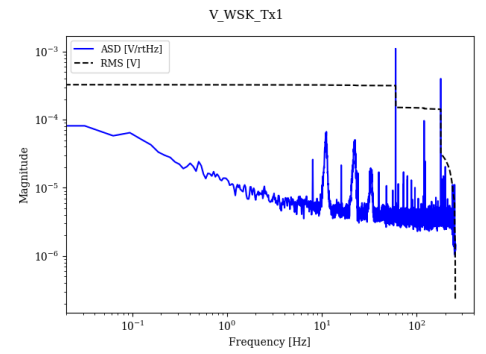
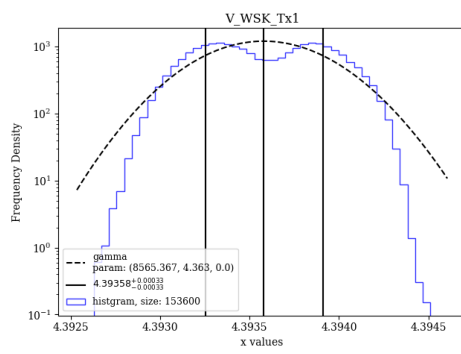
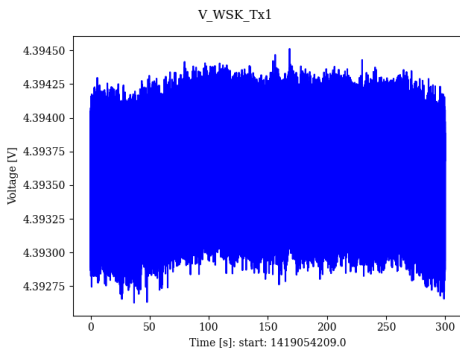
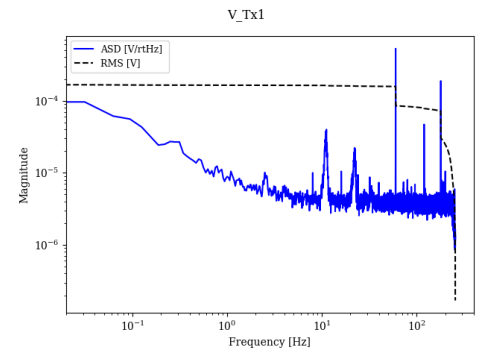
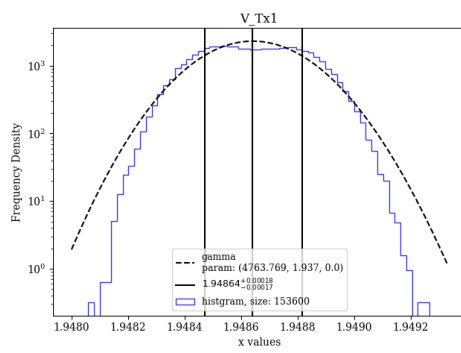
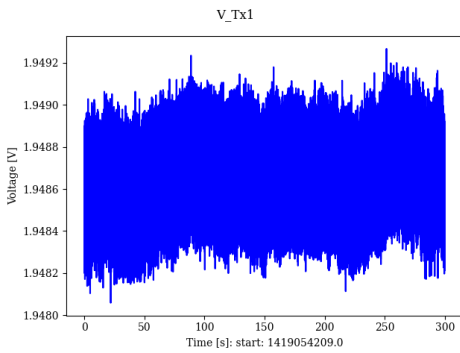
# WSK\_WITH\_PATH\_1\_AT\_RX



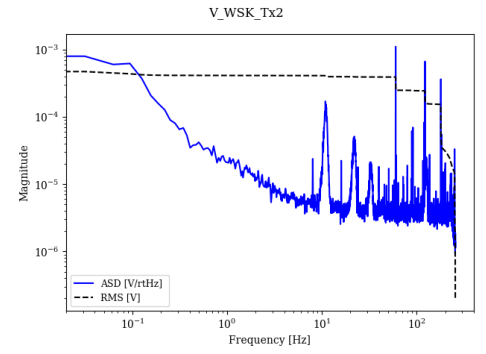
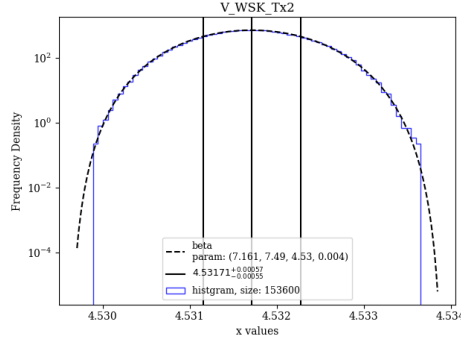
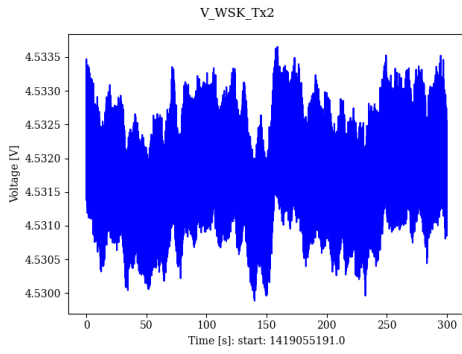
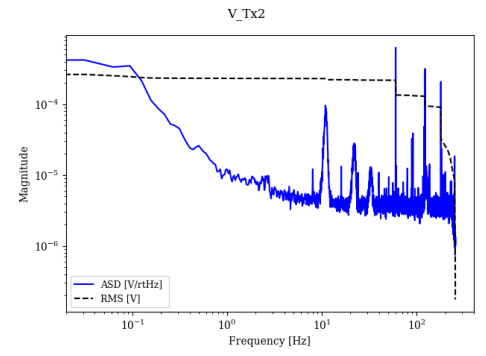
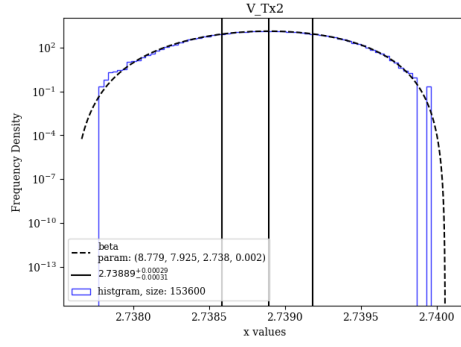
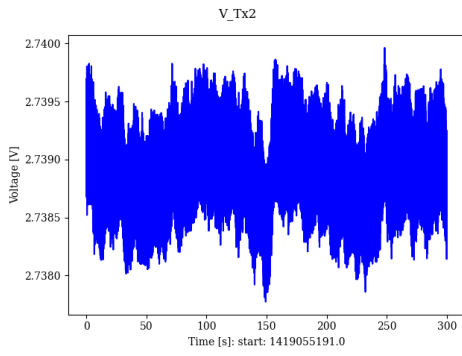
## WSK\_WITH\_PATH\_2\_AT\_RX



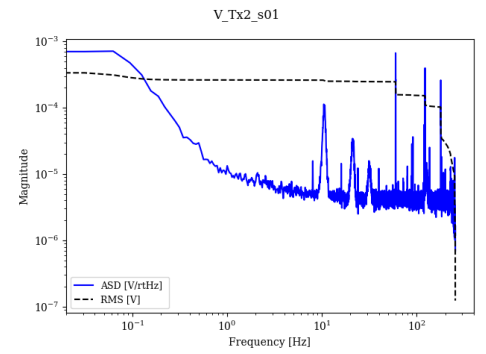
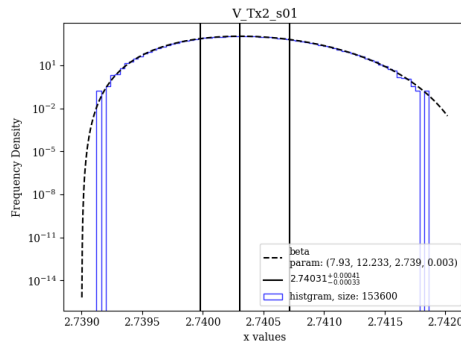
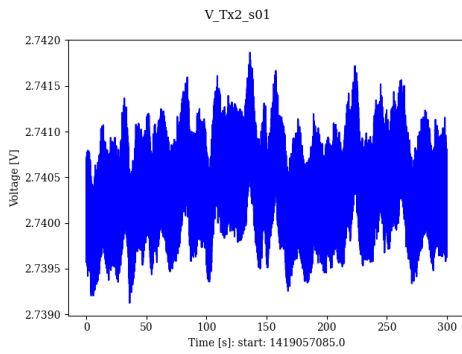
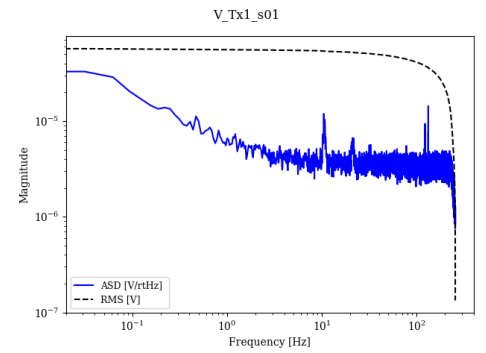
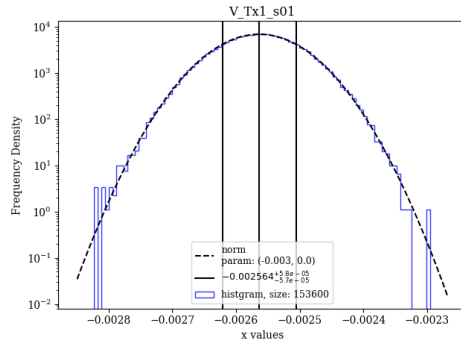
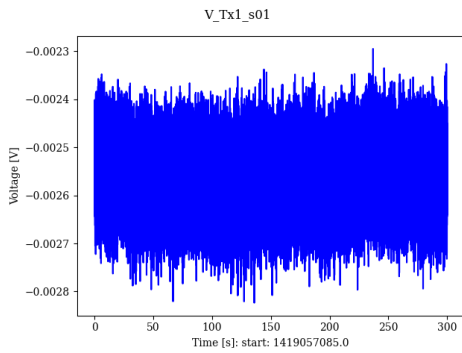
## TXPD1\_AND\_WSK



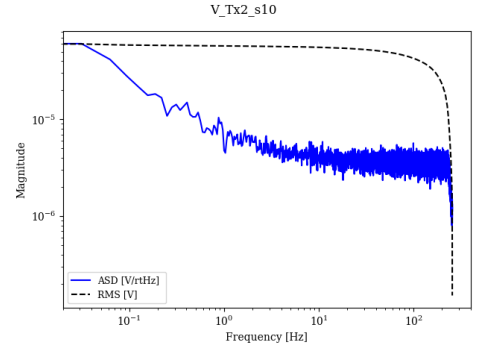
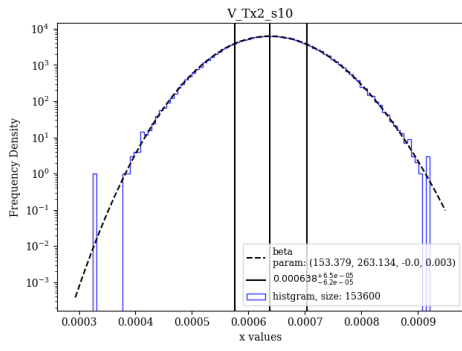
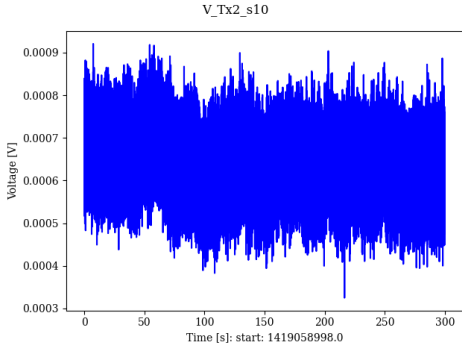
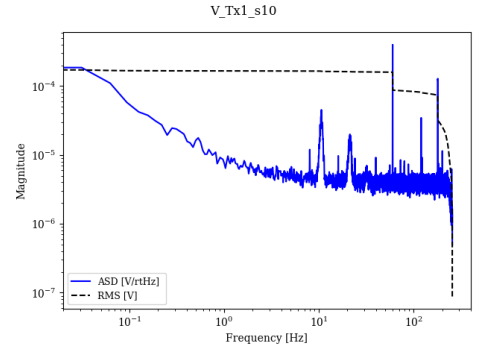
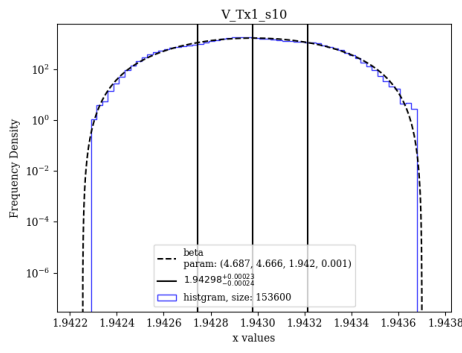
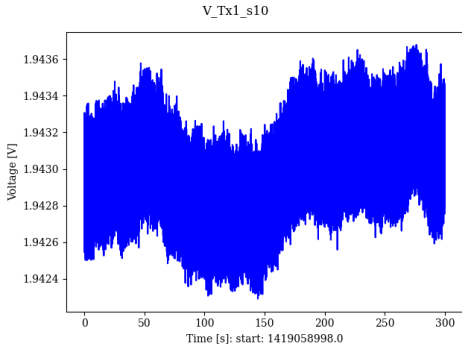
## TXPD2\_AND\_WSK



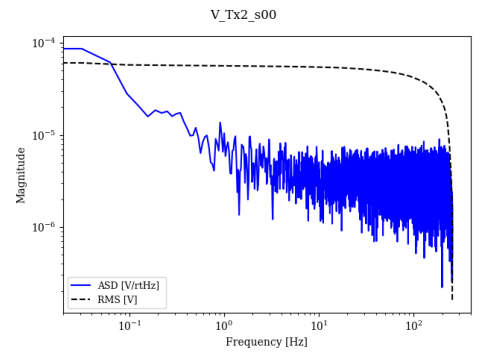
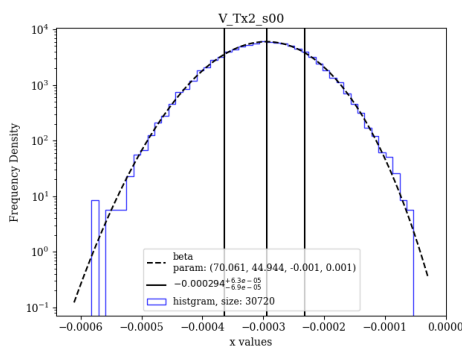
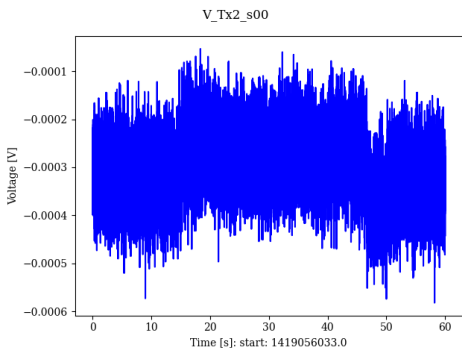
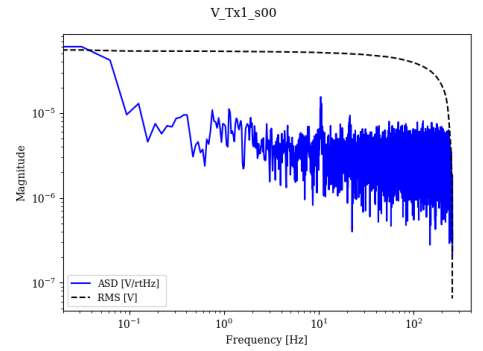
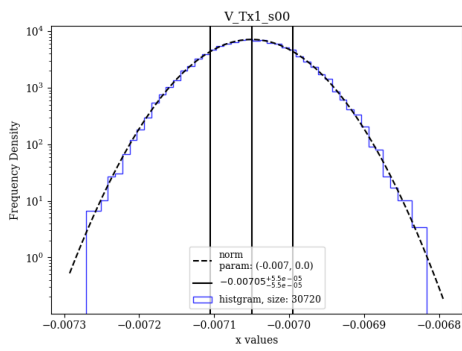
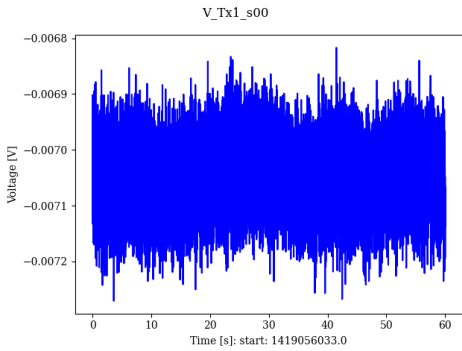
## TXPD1\_AND\_TXPD2\_S01



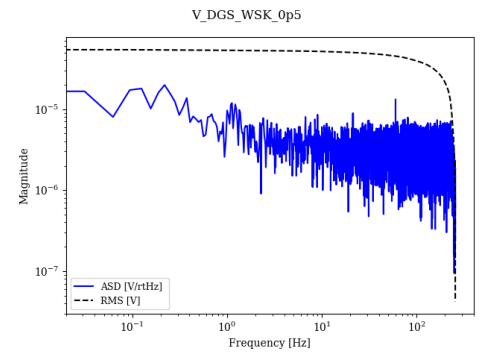
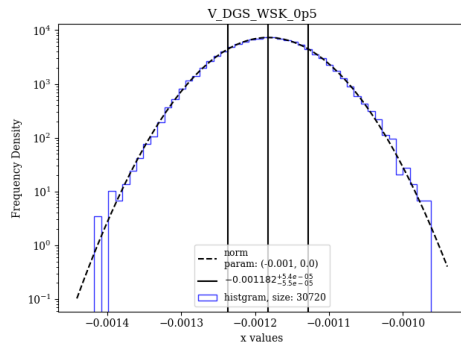
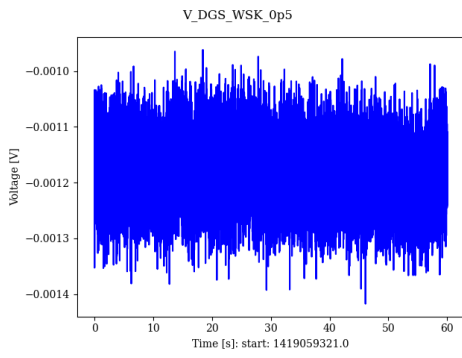
## TXPD1\_AND\_TXPD2\_S10



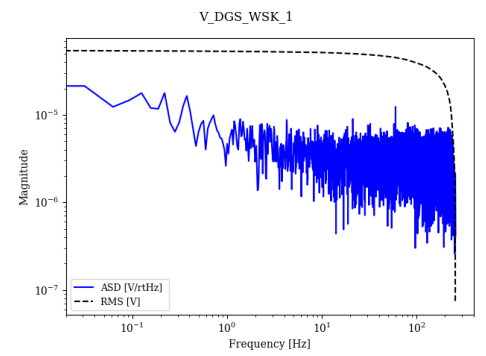
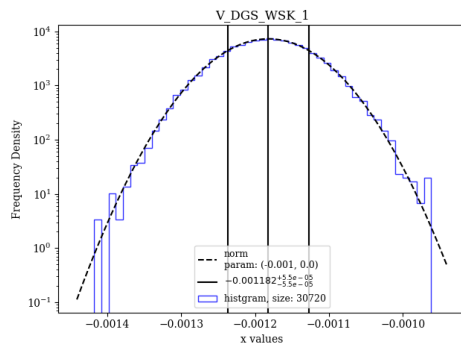
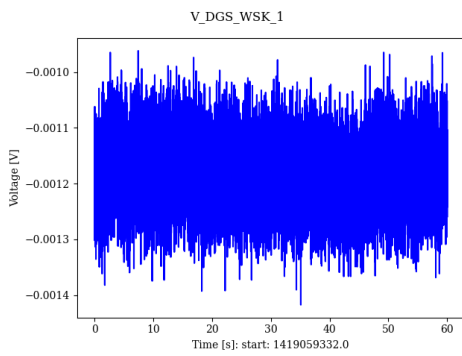
## TXPD1\_AND\_TXPD2\_FOR\_TS60



## WSK\_PORT\_0P5



## WSK\_PORT\_1



## WSK\_PORT\_5

